

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	142	cpu and clock adj generat\$3 and bus and active adj state and standby adj state	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:11
L2	45	1 and access same memory same active same clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:38
L3	12	2 and clock with stop\$3 with standby	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:19
L4	0	3 and voltage near1 generat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:20
L5	11	3 and voltage with bit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:22
L6	0	3 and voltage with source adj linebit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:22
L7	0	3 and voltage with source adj line	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:23
L8	572	365/229.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:39

L9	3322	365/233.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:39
L10	3875	8 or 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:39
L11	3875	8 or 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:39
L12	27	11 and standby and active adj (mode or state) and cpu and bus and stop\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:40
L13	8	12 and voltage with bit adj line\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 13:49
L14	5	voltage with potential with bit adj lines with source adj lines with equal\$4.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/07 14:17